

ITW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/518,642 Confirmation No. : 2849
First Named Inventor : Mitsuhiro YUASA
Filed : March 15, 2005
TC/A.U. : 2815
Examiner : Michael H. Durbin

Docket No. : 101249.55749US
Customer No. : 23911

Title : Mems Array, Manufacturing Method Thereof, and Mems
Device Manufacturing Method Based on the Same

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 25, 2007, Applicants hereby elect Group I, claims 1-16 drawn to the semiconductor device.

Additionally, Applicants elect Species 1, which the Examiner alleges is drawn to an array of passive components connected by transistors. Regarding this species, claims 1-2, 4-11 and 16 are readable thereon. At least claims 1 and 16 are generic.

If there are any questions regarding this response or the application in general, a telephone call to the undersigned would be appreciated since this should expedite the prosecution of the application for all concerned.

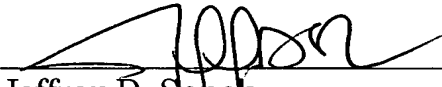
If necessary to effect a timely response, this paper should be considered as a petition for an Extension of Time sufficient to effect a timely response, and

Serial No. 10/518,642
Amendment Dated: August 20, 2007
Reply to Office Action Mailed: July 25, 2007
Attorney Docket No. 101249.55749US

please charge any deficiency in fees or credit any overpayments to Deposit
Account No. 05-1323 (Docket #101249.55749US).

Respectfully submitted,

August 20, 2007



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